



J. Jones
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PATENT
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FEB 05 2003

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Sung Gue LEE et al. Conf.: 6825

Appl. No.: 10/043,146 Group: Unassigned

Filed: January 14, 2002 Examiner: Unassigned

For: HOLE PLUGGING METHOD FOR PRINTED CIRCUIT
BOARDS, AND HOLE PLUGGING DEVICE

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, DC 20231

FEB 05 2003

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please add the following claims:

--35. A plugging method for a printed circuit board having a plurality of first circuit patterns formed on a surface of the board, a plurality of second circuit patterns formed in the board, comprising the steps of:

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2 FC:1202

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